

Notice of References Cited	Application/Control No. 10/028,881		Applicant(s)/Patent Under Reexamination MACKEY, RICHARD P.	
	Examiner Christopher A Daley		Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,336,158	01-2002	Martwick, Andrew W.	710/104
*	B	US-6,260,094	07-2001	Davis et al.	710/314
*	C	US-6,047,349	04-2000	Klein, Dean A.	710/315
*	D	US-5,568,621	10-1996	Wooten, David R.	710/112
*	E	US-6,230,225	05-2001	Olarig et al.	710/306
*	F	US-5,864,688	01-1999	Santos et al.	710/306
*	G	US-5,859,988	01-1999	Ajanovic et al.	710/306
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	K Annamalai, Multi-ported PCI-to-PCI Bridge chip, November, 1997, Wescon/97 Conference Proceedings, pages 426 - 433			
	V	K. Wang et al, Designing the MPC105 PCI bridge/memory controller, April 1995, Micro, IEEE, pages 44 - 49			
	W	W. Bainbridge et al, Asynchronous macrocell interconnect using Marble, March 1998, Advanced Research in Asynchronous Circuits Proceedings, 1998, pages 122- 132			
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.